

IN THE CLAIMS:

Please add new claim 9 as follows, and amend claims 1-2, and 5-6 as follows:

1. (Currently Amended) A compiler device to generate an object code corresponding to a plurality of procedures in a source program, said plurality of procedures using an interface area in common, said object code performing said plurality of procedures in parallel with a plurality of threads, one of said plurality of threads being a master thread and the others being non-master threads, said object code dynamically allocating private interface areas ~~corresponding to~~, different from said interface area in common, for said non-master threads, when said plurality of threads are processed in parallel.

2. (Currently Amended) A compiler device comprising:
code generating means for generating an object code corresponding to a plurality of procedures in a source program, said plurality of procedures using an interface area in common, said object code being performed in parallel with a plurality of threads, one of said plurality of threads being a master thread and the others being non-master threads, said object code determining leading addresses of private interface areas which being dynamically allocated for said non-master threads instead of the interface area used in common and allocated to said master thread, when at least one of said plurality of threads is processed; and

code converting means for converting direct references to data in the interface area in the object code into indirect references to data in the private interface areas based on the leading addresses of the private interface areas.

3. (Previously Presented) A compiler device of claim 2, for determining the leading address of the interface area that is dynamically allocated for each thread.

4. (Previously Presented) A compiler device of claim 2, wherein said code generating means is constituted to generate a code for determining the leading address of the interface area designated by a user.

5. (Currently Amended) A computer-readable recording medium recorded with a compiler program for causing a computer to realize a function to generate an object code corresponding to a plurality of procedures in a source program, said plurality of procedures using an interface area in common, said object code being performed in parallel with a plurality of threads, one of said plurality of threads being a master thread and the others being non-master threads, said object code dynamically allocating private interface areas ~~corresponding to~~ different from said interface area in common, for said non-master threads, when said plurality of threads are executed.

6. (Currently Amended) A computer-readable recording medium recorded with a compiler program for causing a computer to realize:

an object code generating function for generating an object code corresponding to a plurality of procedures in a source program, said plurality of procedures using an interface area in common, said object code being performed with a plurality of threads, one of said plurality of threads being a master thread and others being non-master threads, said object code determining leading addresses of private interface areas which are dynamically allocated for the non-master threads instead of the interface area used in common and allocated to said master thread, when one of the plurality of threads is processed; and

a code converting function for converting direct references to data in the interface area in the object code into indirect references to data in the private interface areas based on the leading addresses of the private interfaces areas.

7. (Previously Presented) A computer-readable recording medium recorded with a compiler program of claim 6, wherein said code generating function is constituted to generate a code for calling a library for determining the leading address of the interface area that is dynamically allocated for each thread.

8. (Previously Presented) A computer-readable recording medium recorded with a compiler program of claim 6, wherein said code generating function is constituted to generate a code for determining the leading address of the interface area designated by a user.

9. (New) A compiler device to generate an object code corresponding to a plurality of procedures in a source program, said plurality of procedures using an interface area in common, said object code performing said plurality of procedures in parallel with a plurality of threads, one of said plurality of threads being a master thread and the others being non-master threads, said object code dynamically allowing access to said interface area according to whether an active thread is said master thread, and whether common block information for said interface area is new, when said plurality of threads are processed in parallel.